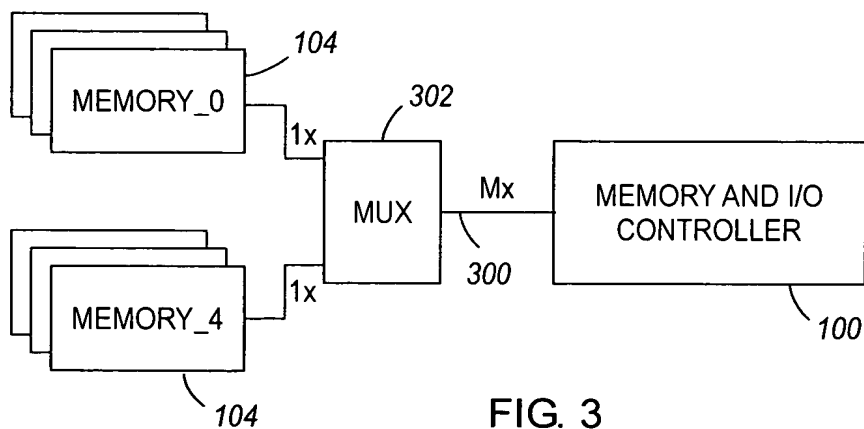
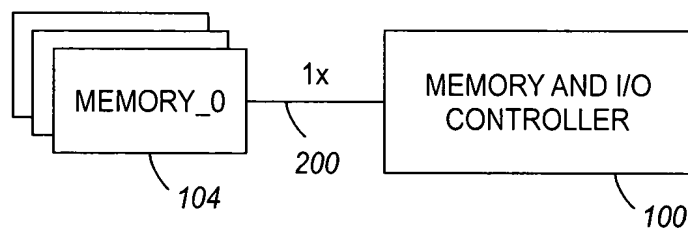
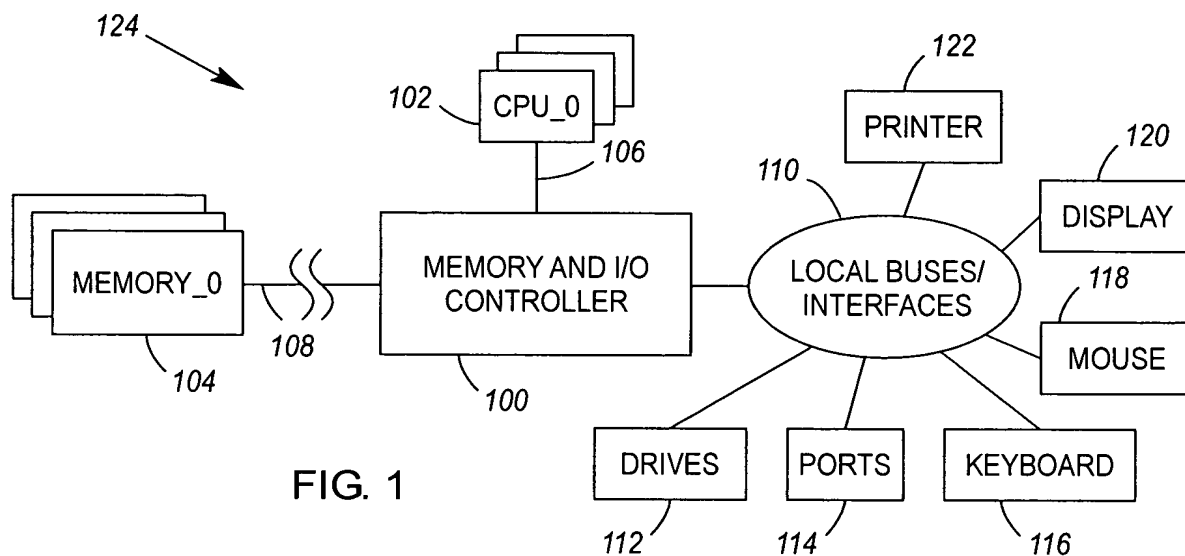


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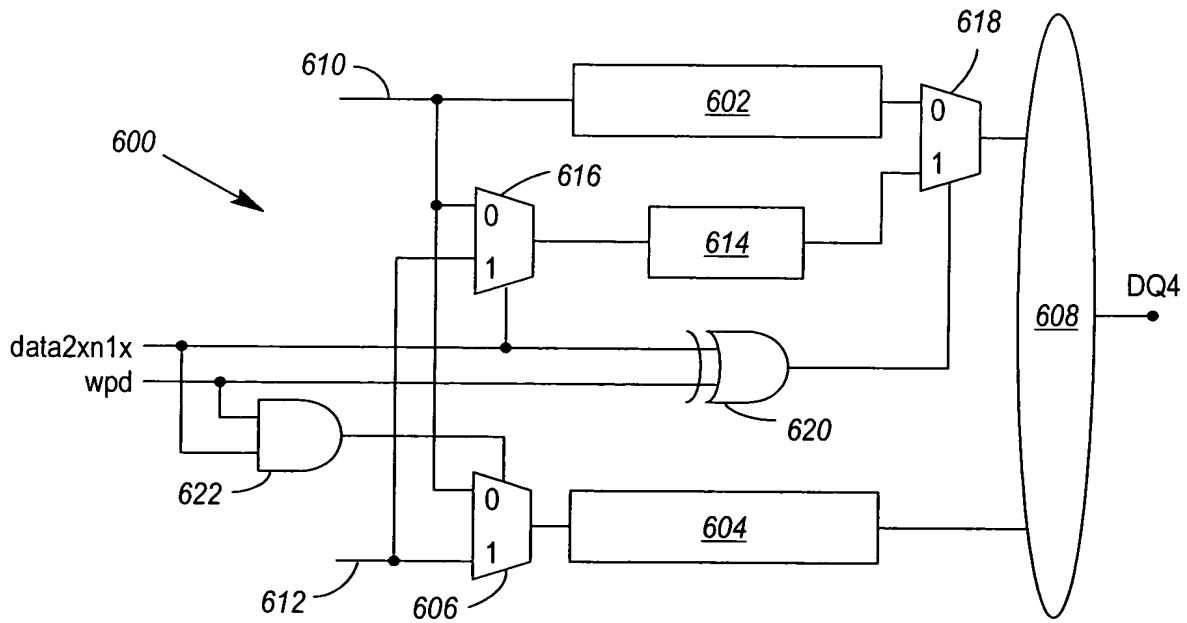
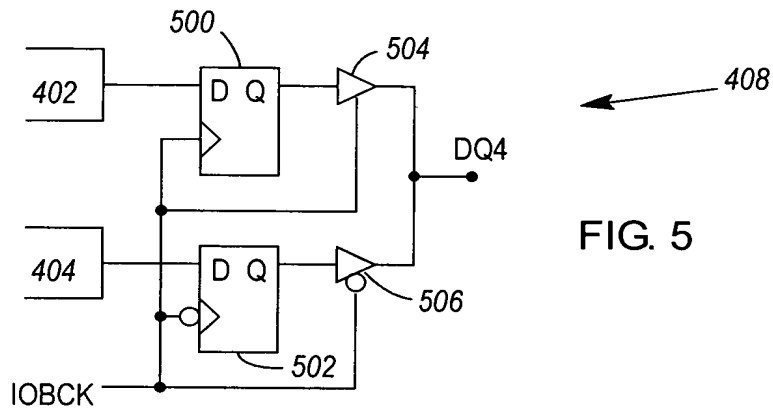
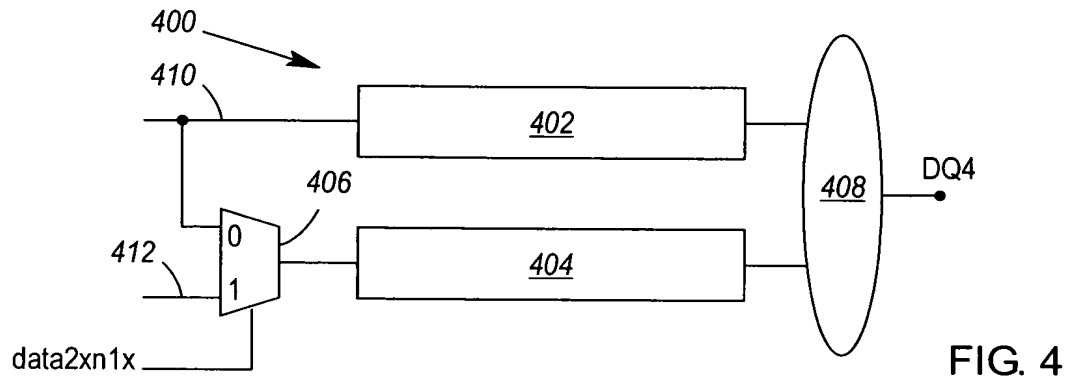


FIG. 6

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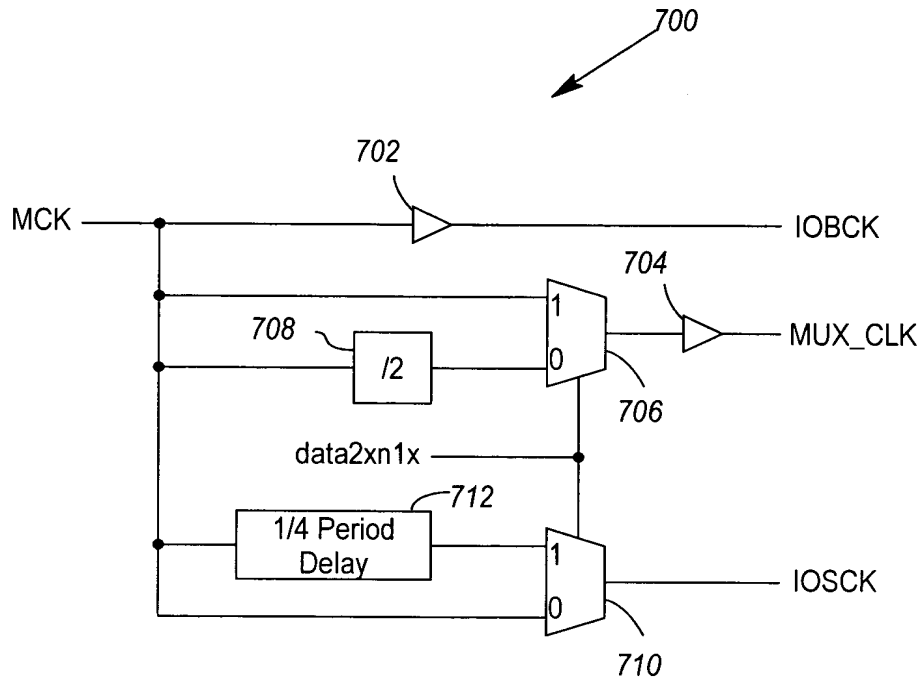


FIG. 7

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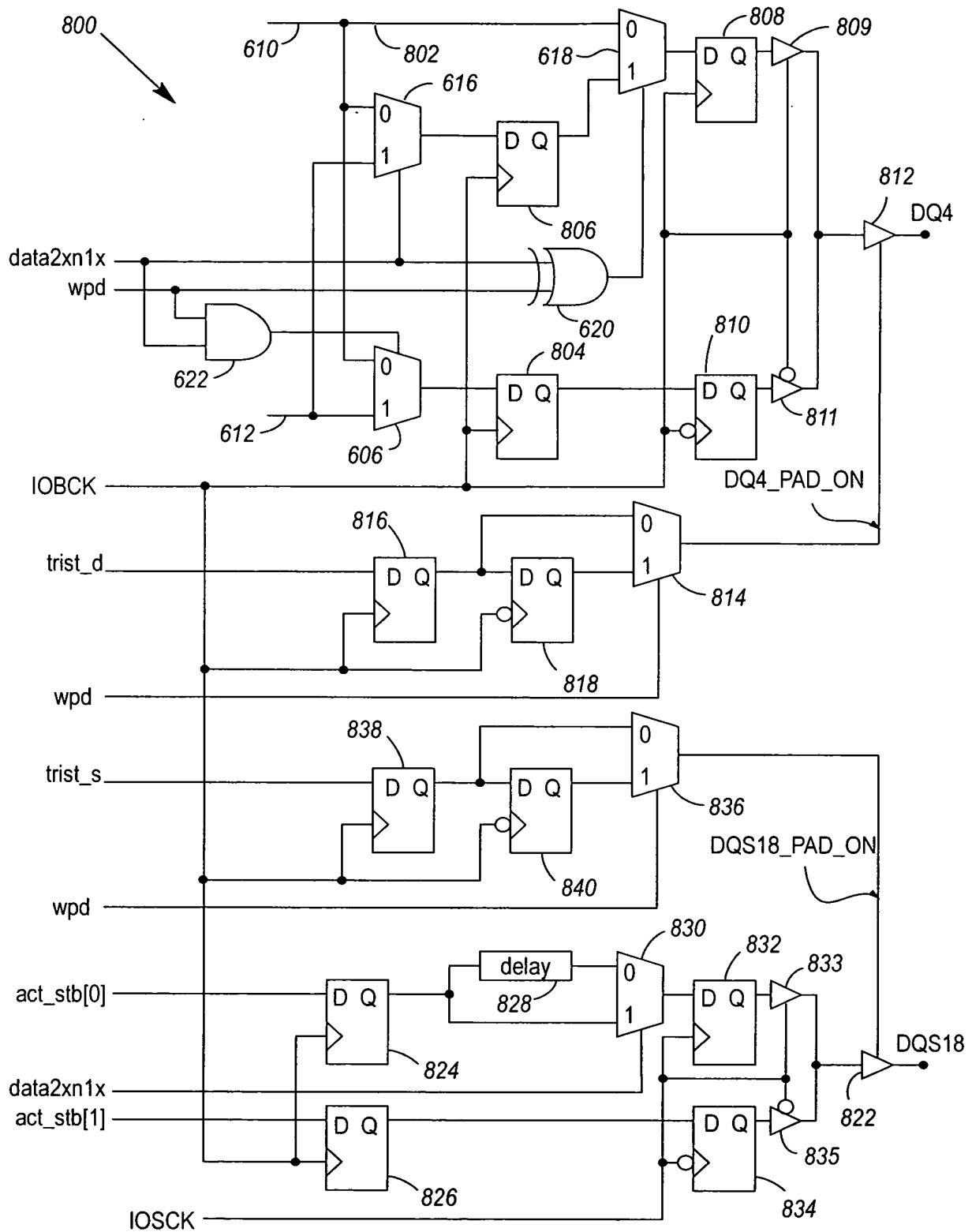
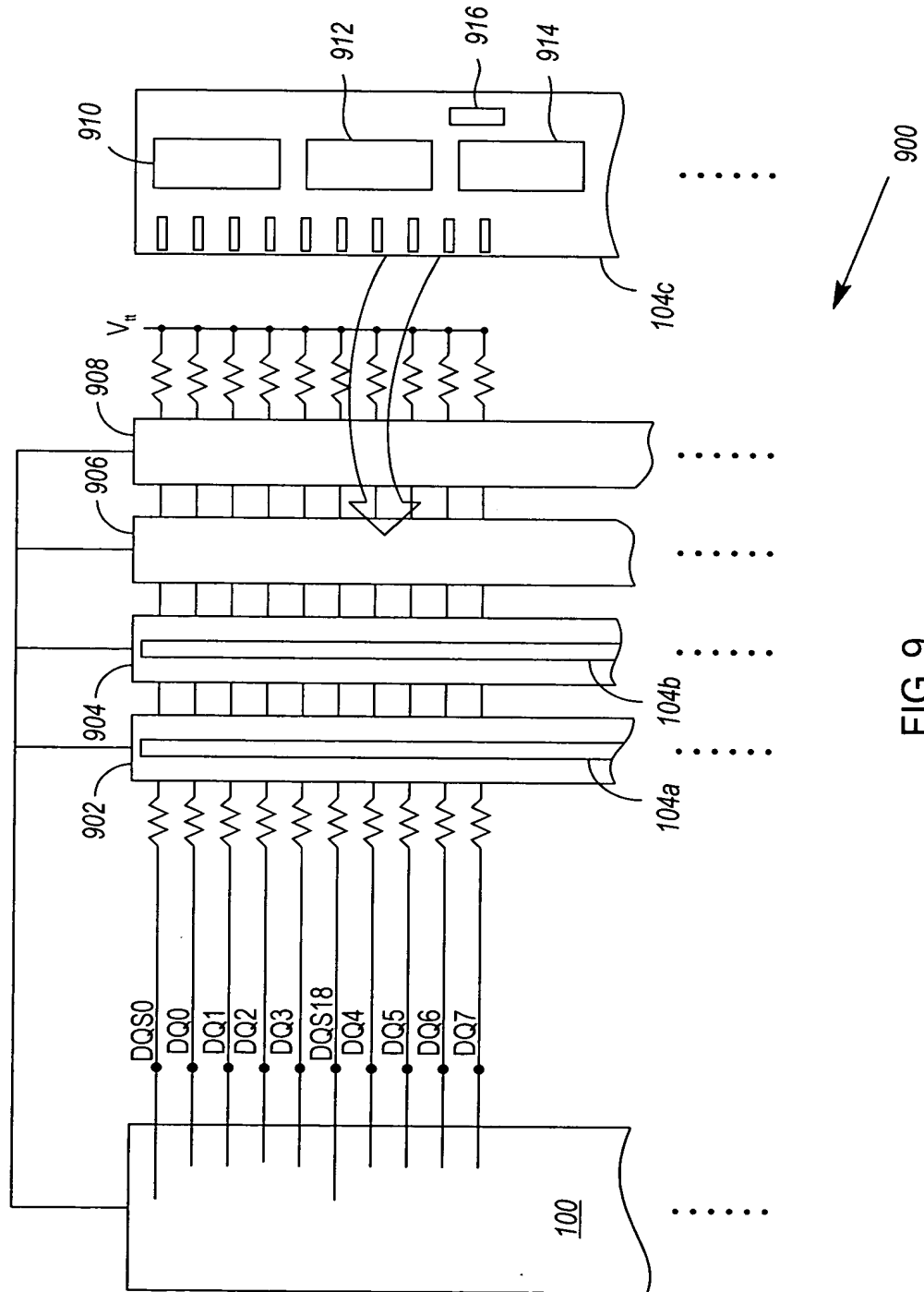


FIG. 8

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STROBE PADS		DATA PADS
x4 DIMM	x8 DIMM	
0	0	3:0
18	0	7:4
1	1	11:8
19	1	15:12
2	2	19:16
20	2	23:20
3	3	27:24
21	3	31:28
•	•	•
•	•	•
•	•	•
16	16	131:128
34	16	135:132
17	17	139:136
35	17	143:140

FIG. 10

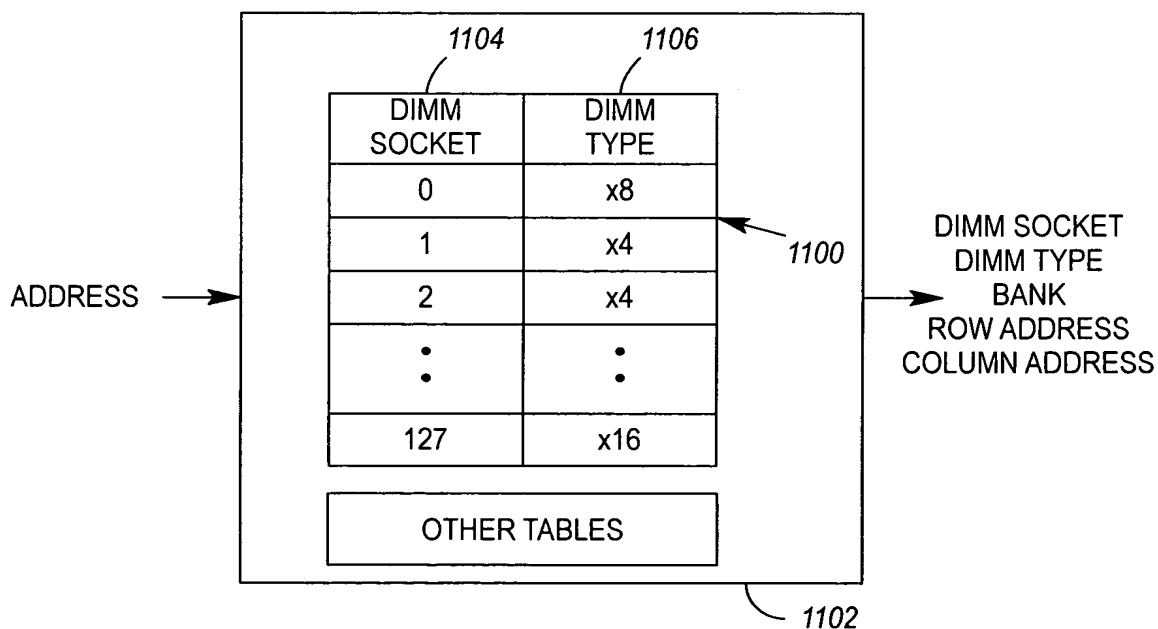


FIG. 11

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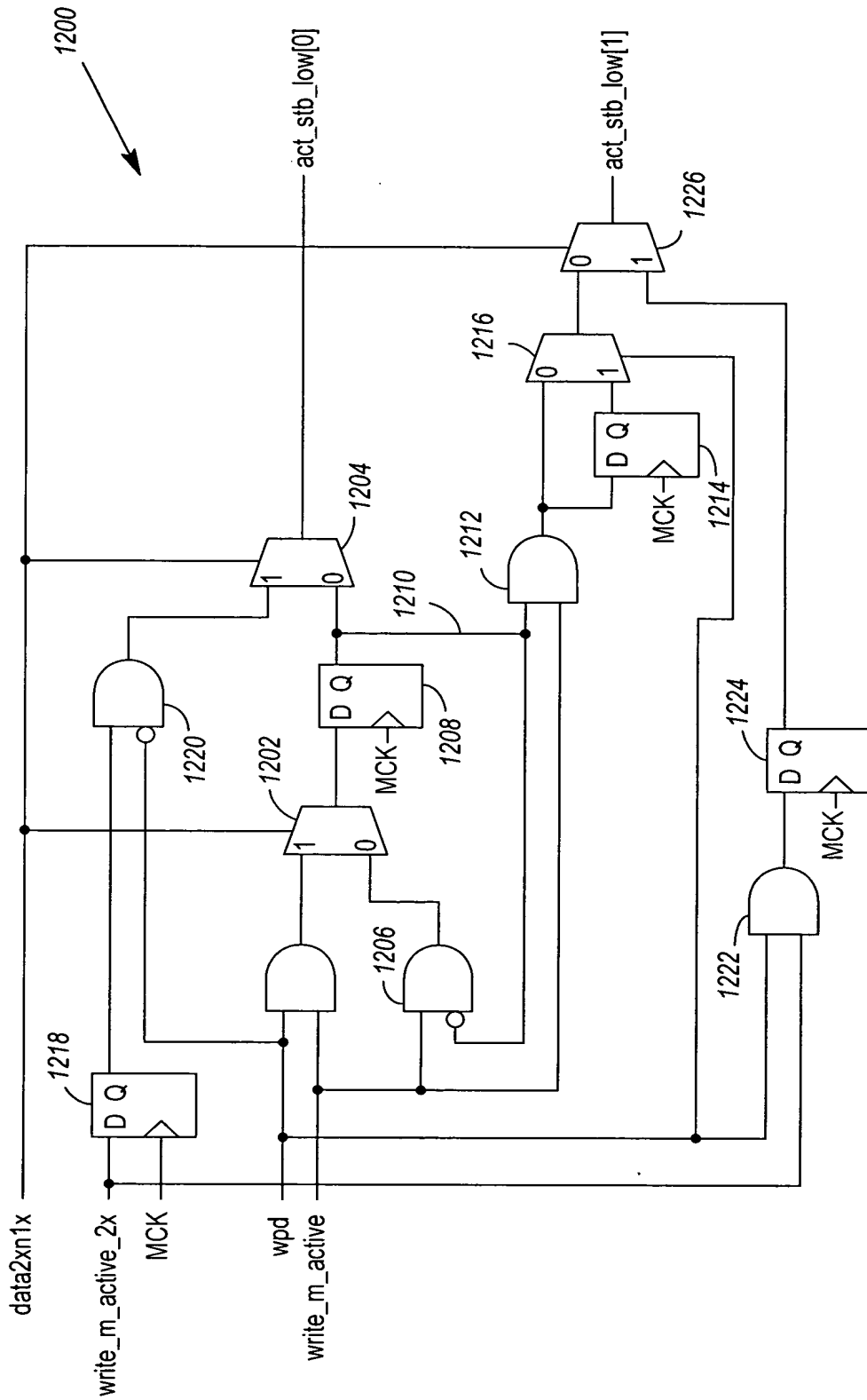


FIG. 12

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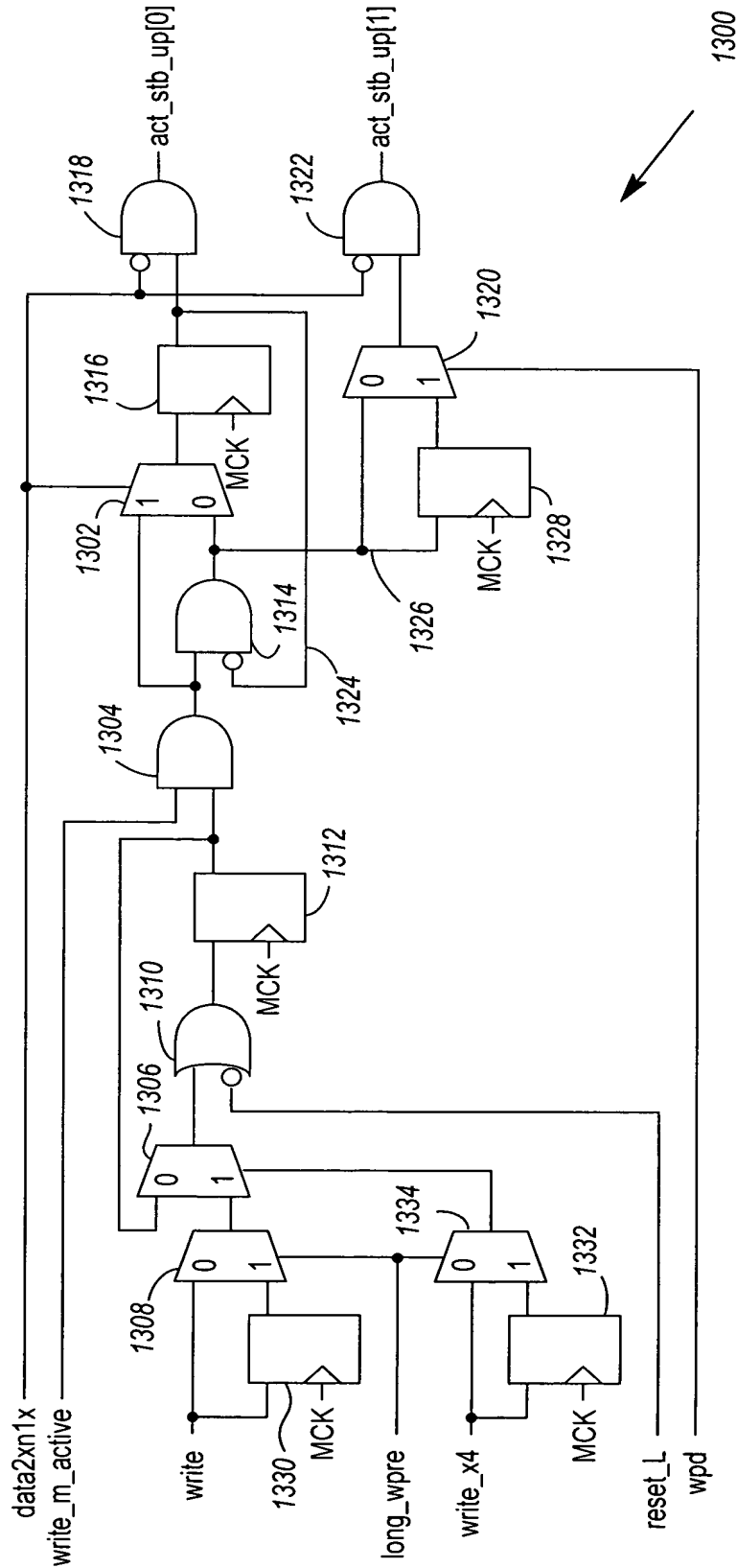


FIG. 13

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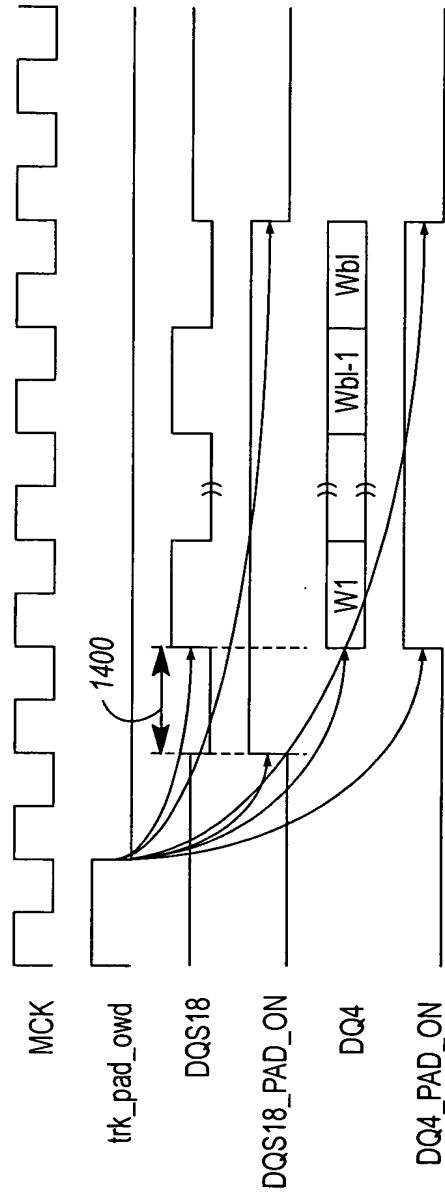


FIG. 14

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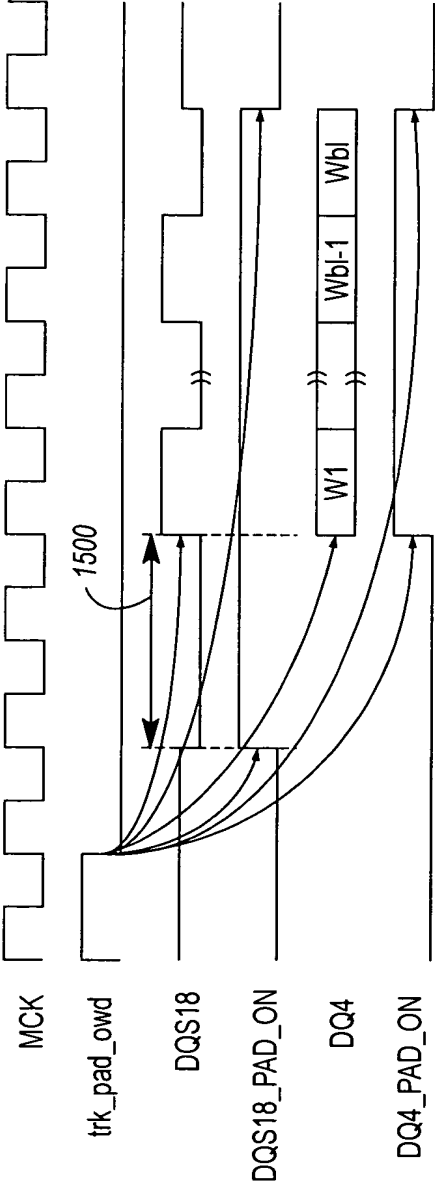


FIG. 15

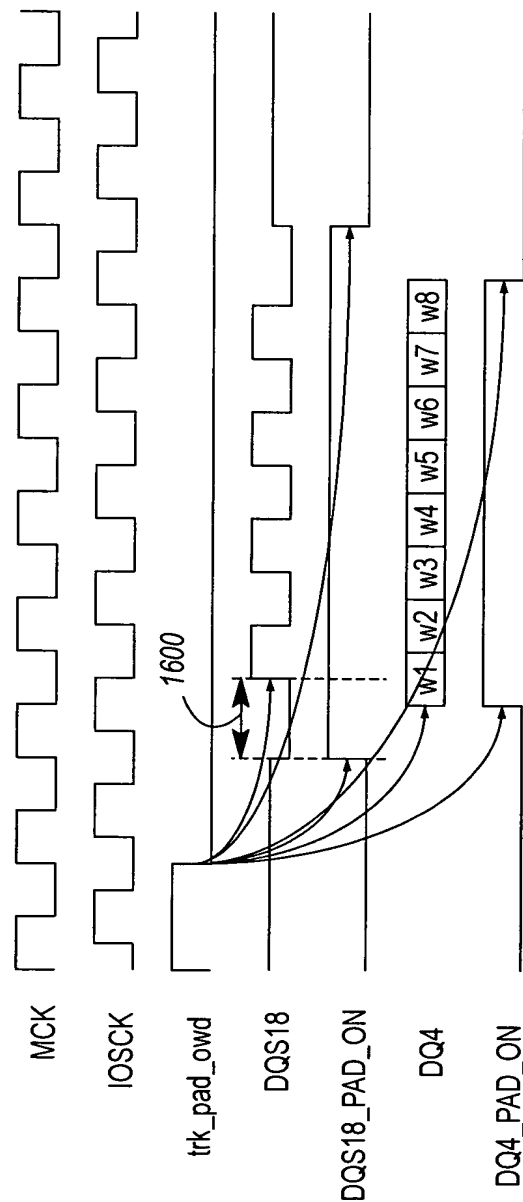


FIG. 16

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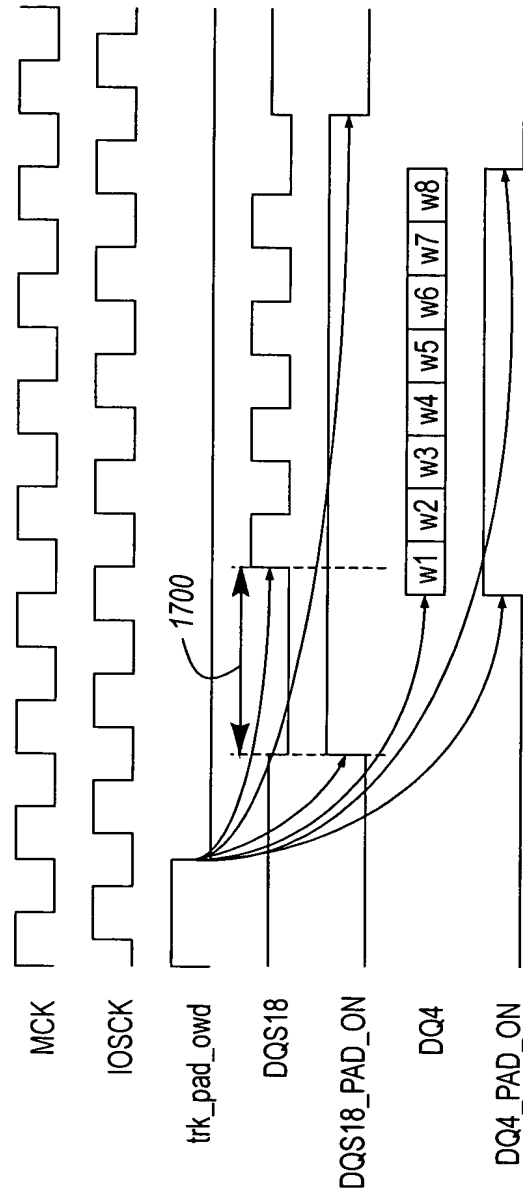


FIG. 17

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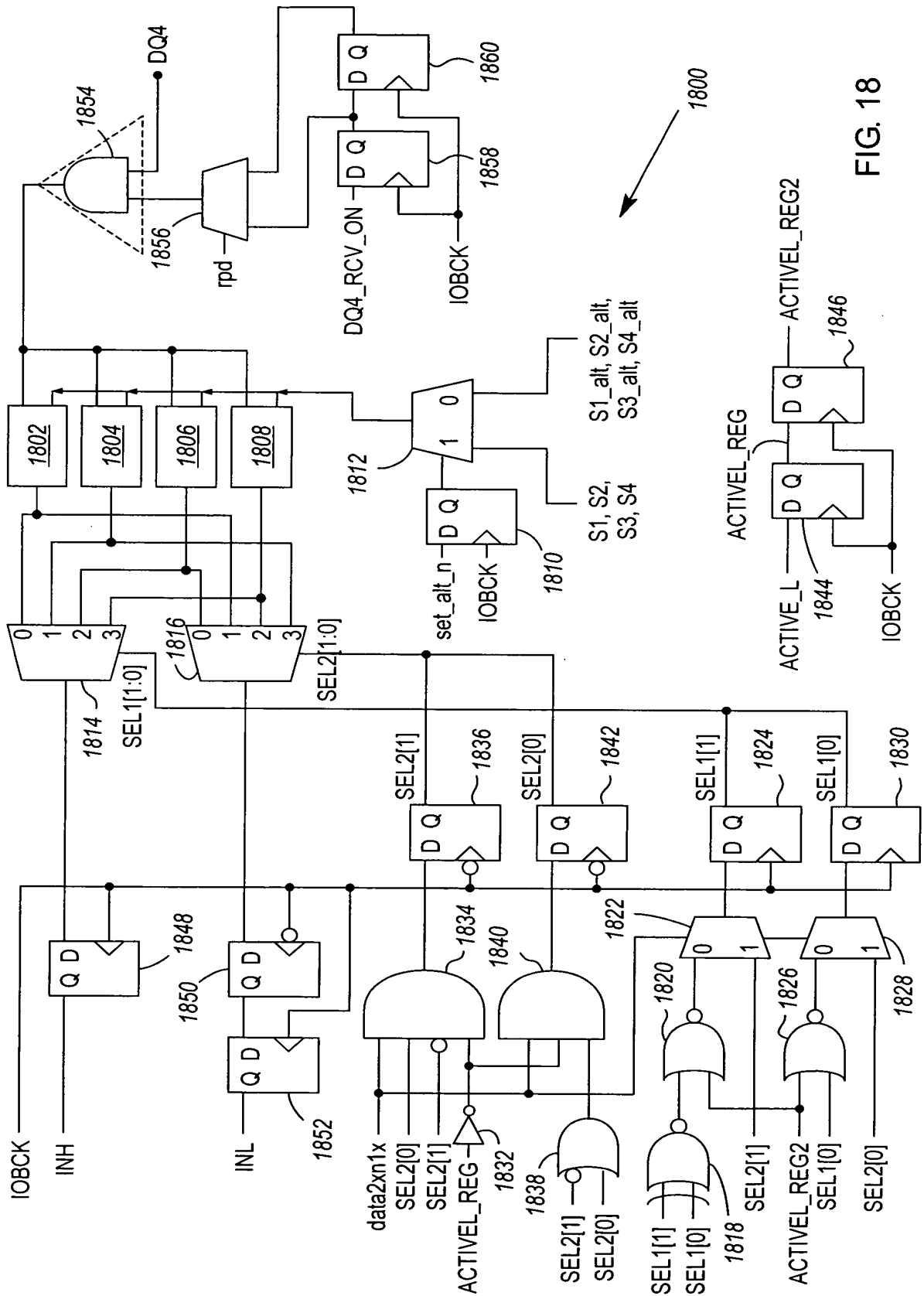


FIG. 18

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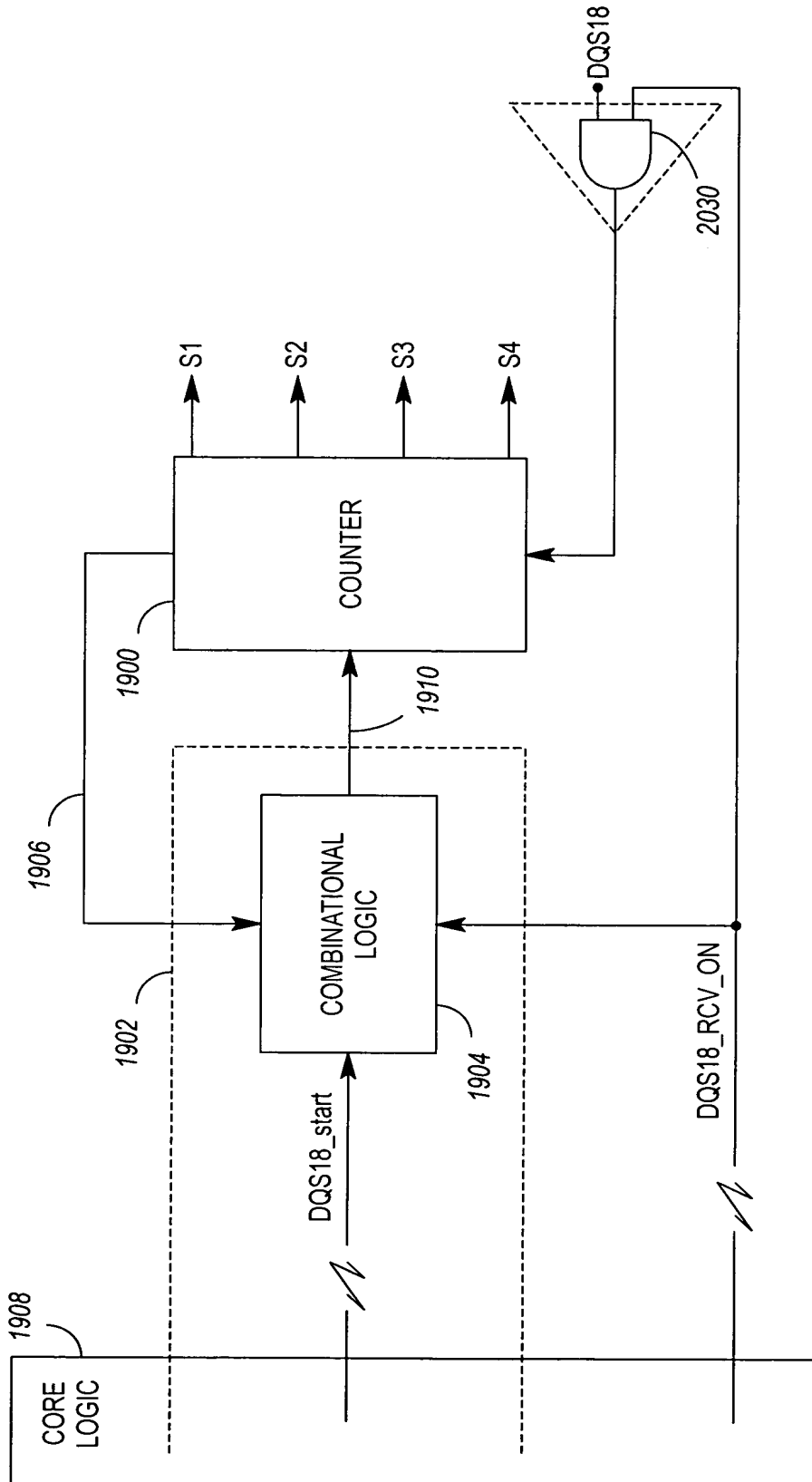
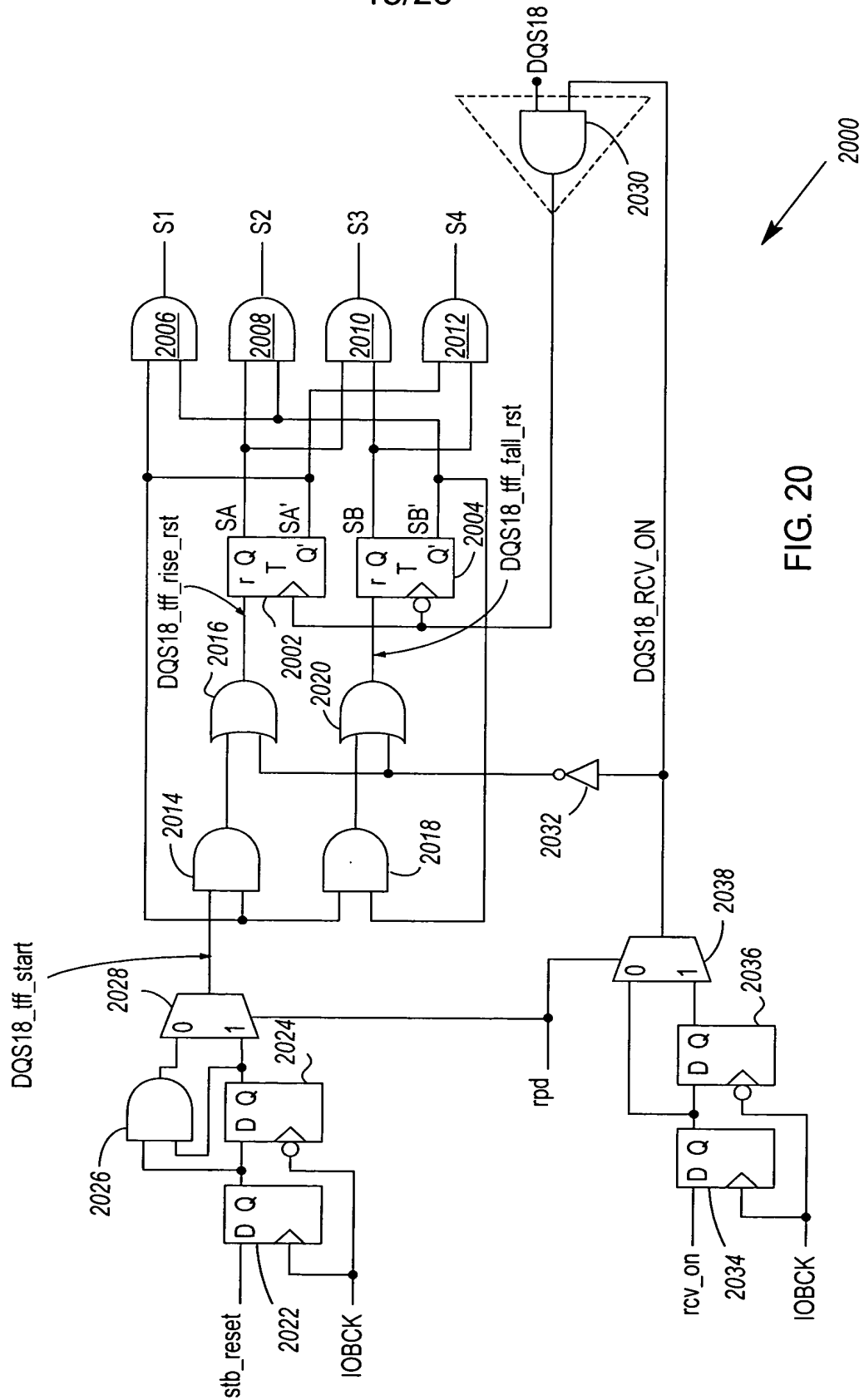


FIG. 19



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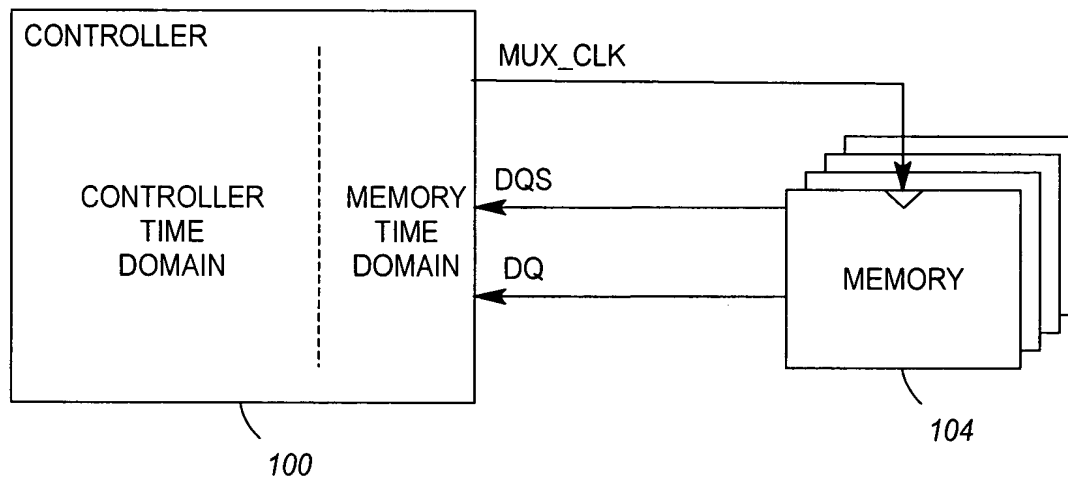


FIG. 21

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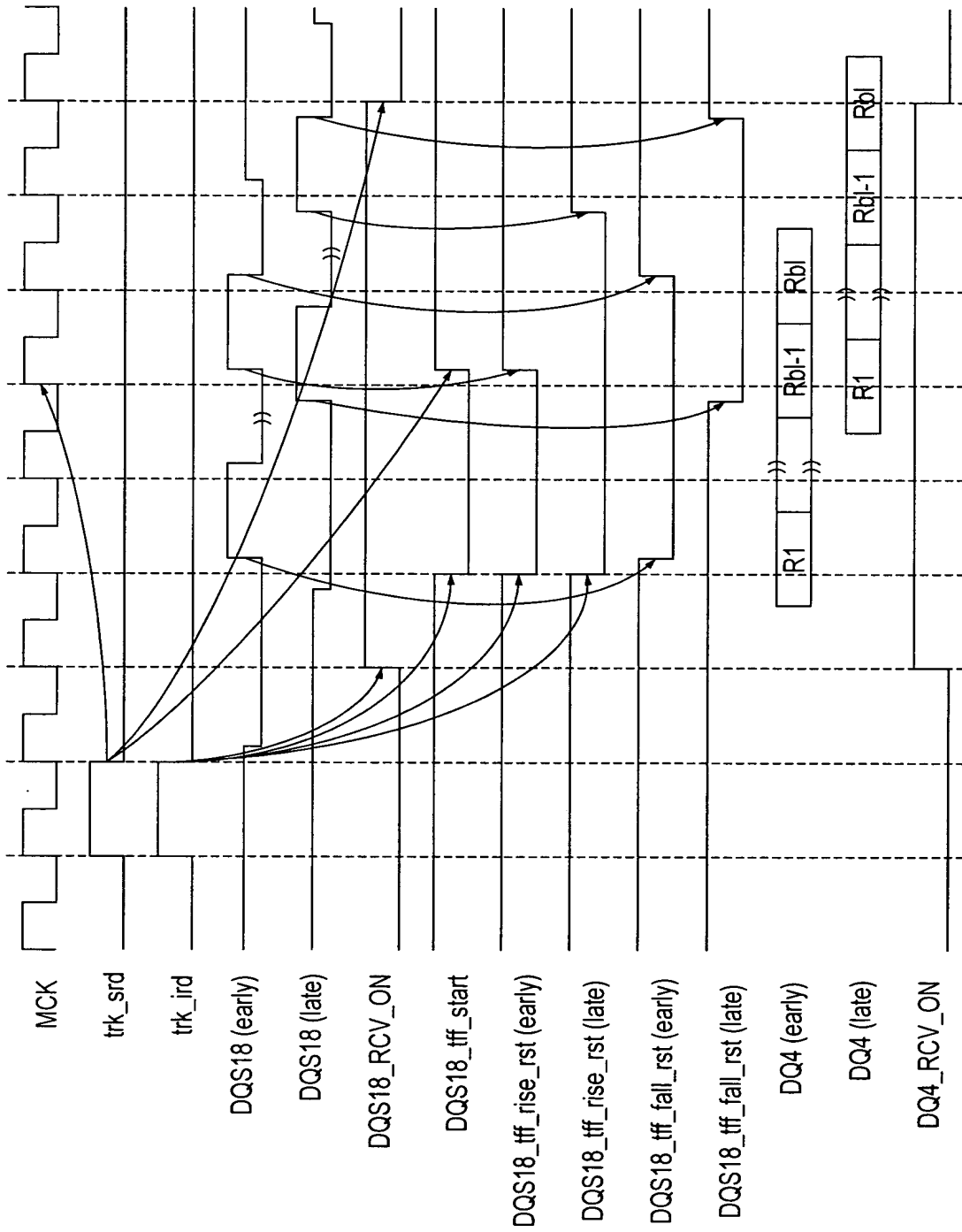


FIG. 22

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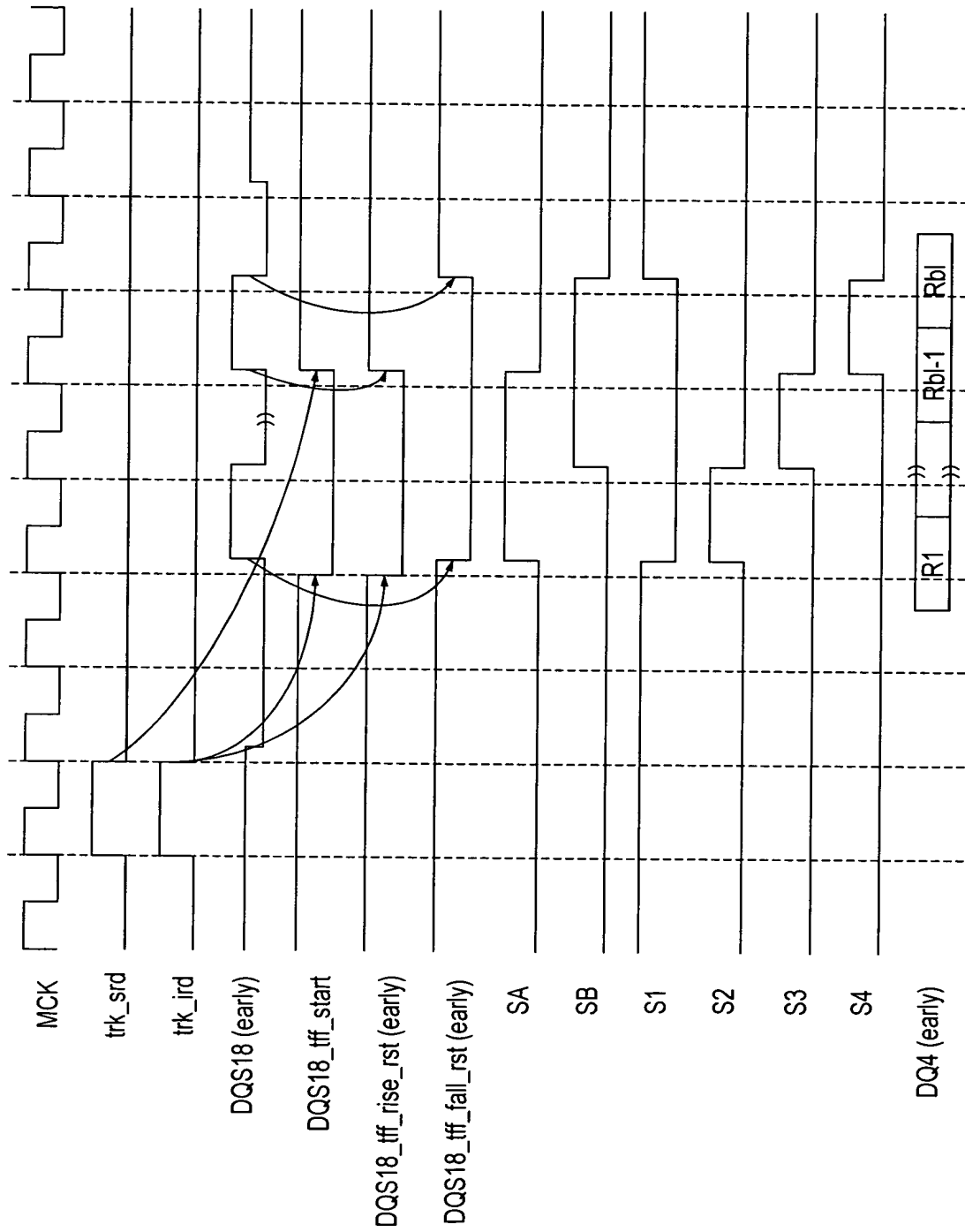


FIG. 23

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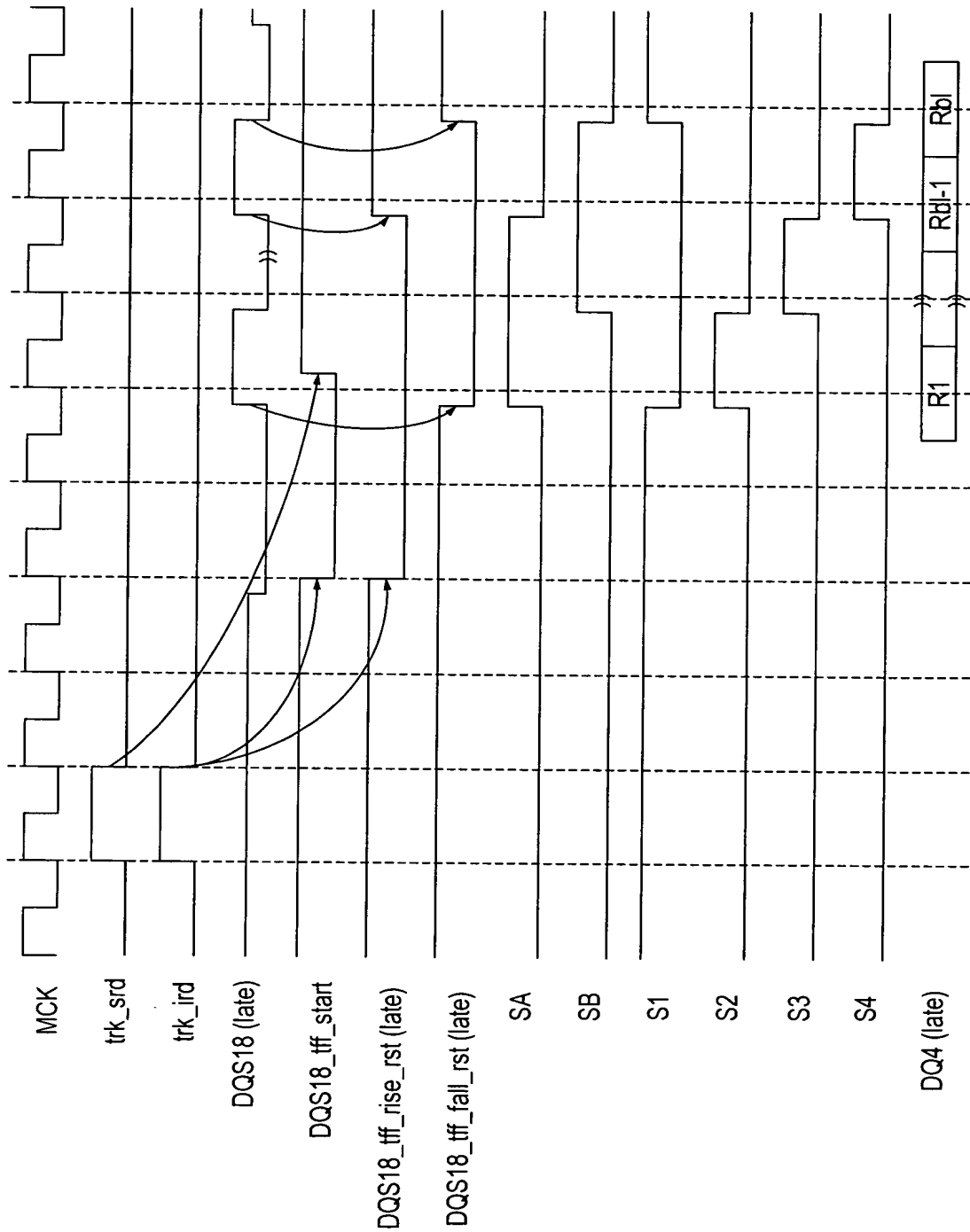


FIG. 24

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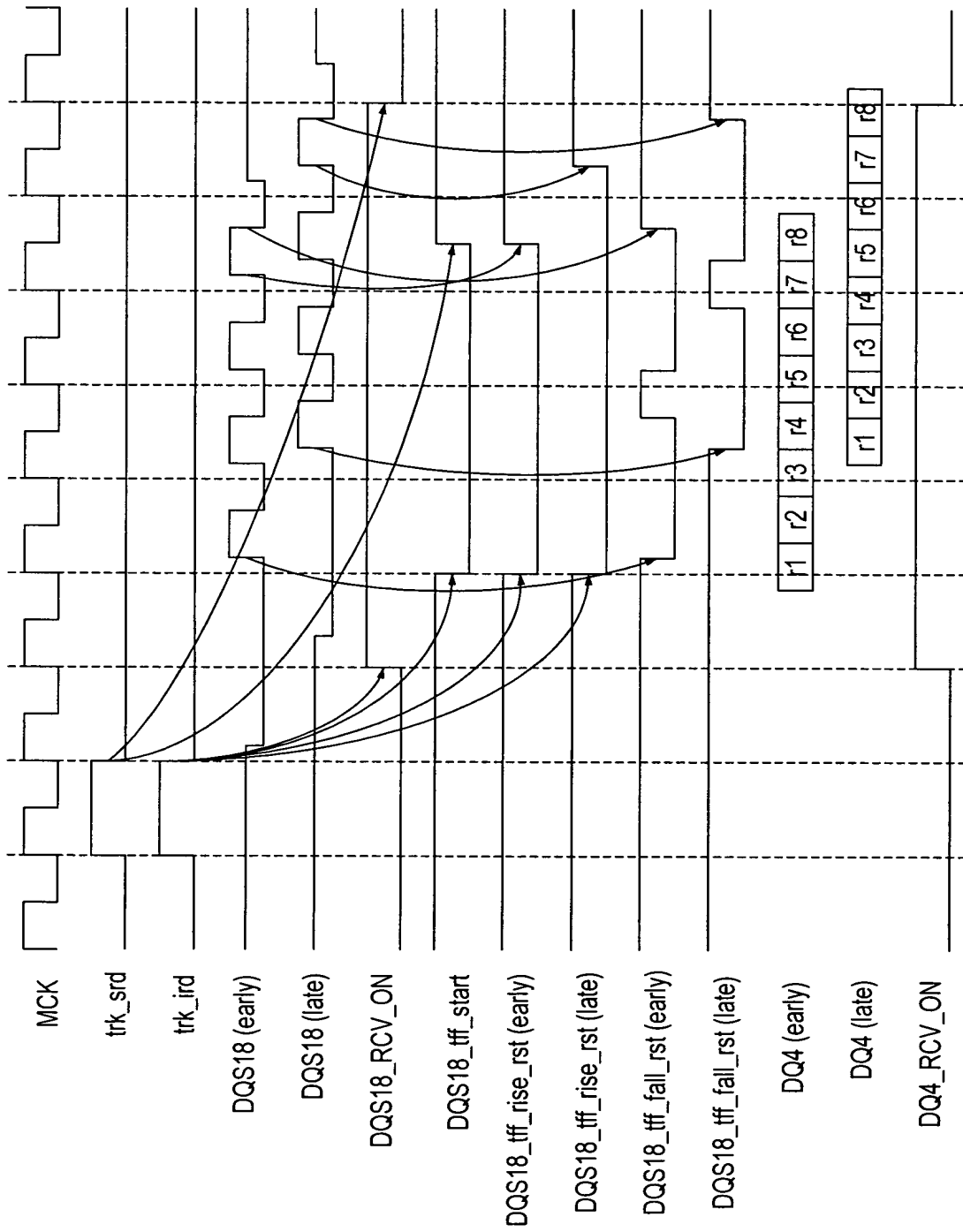


FIG. 25

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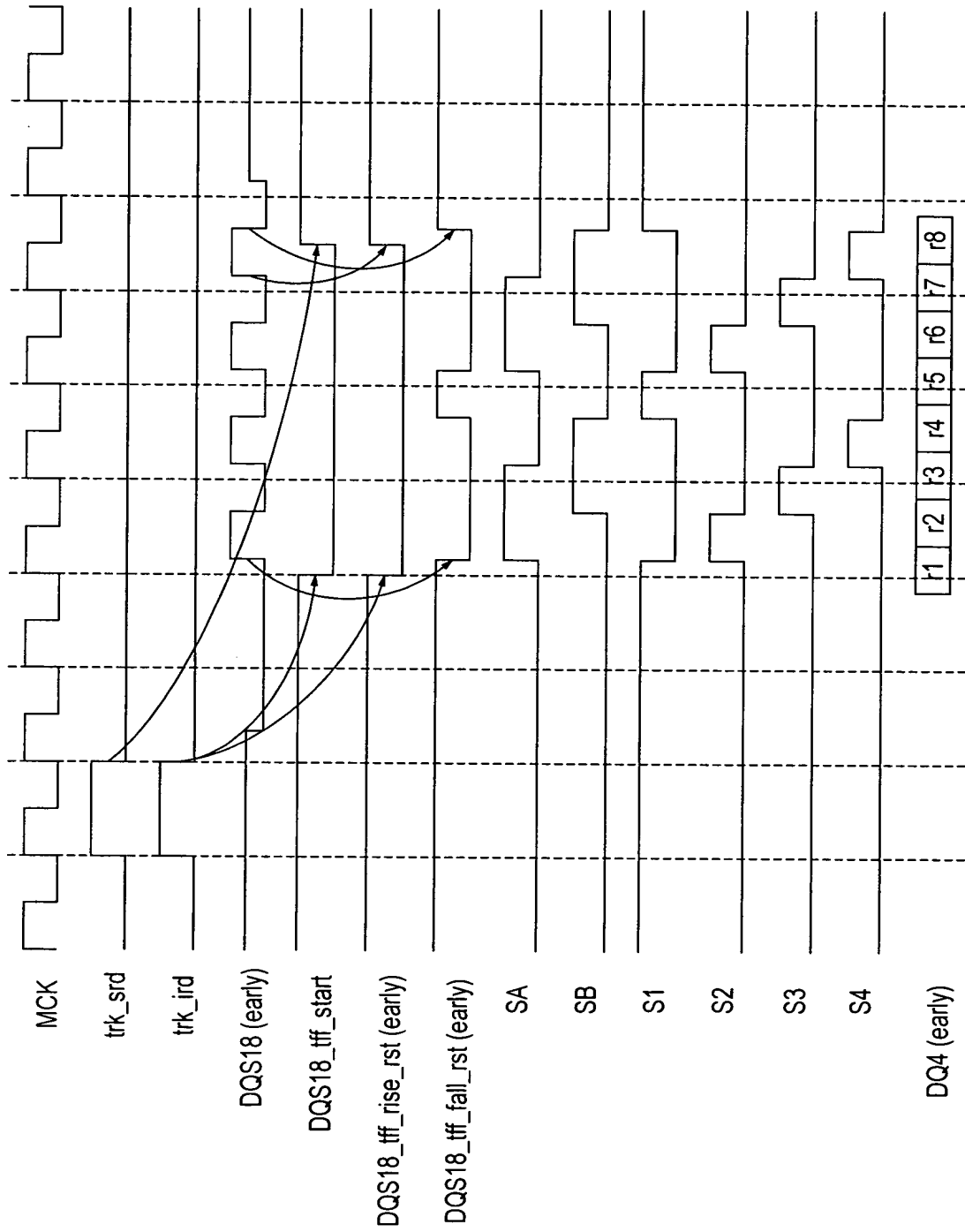


FIG. 26

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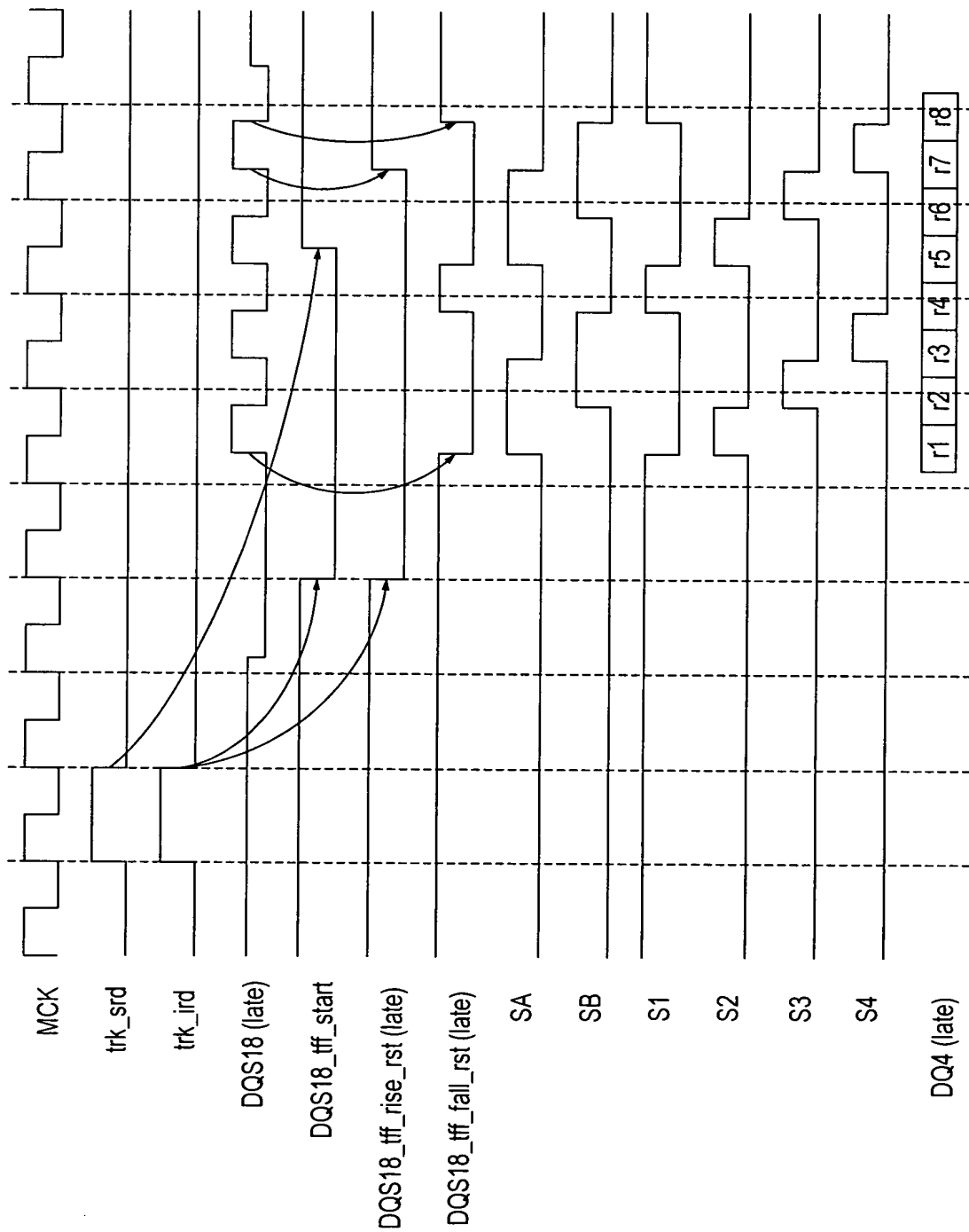


FIG. 27

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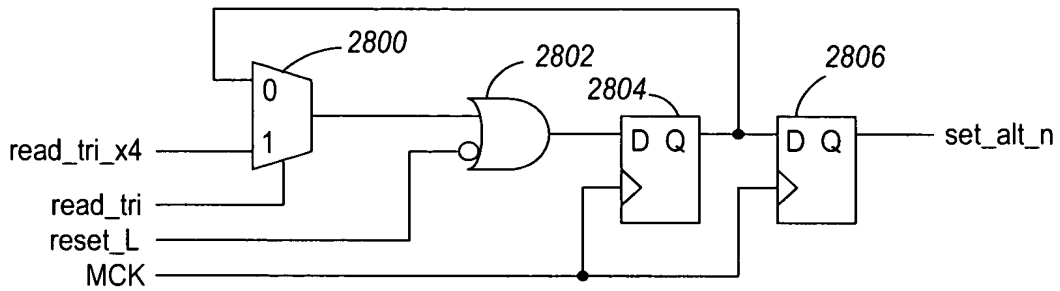


FIG. 28

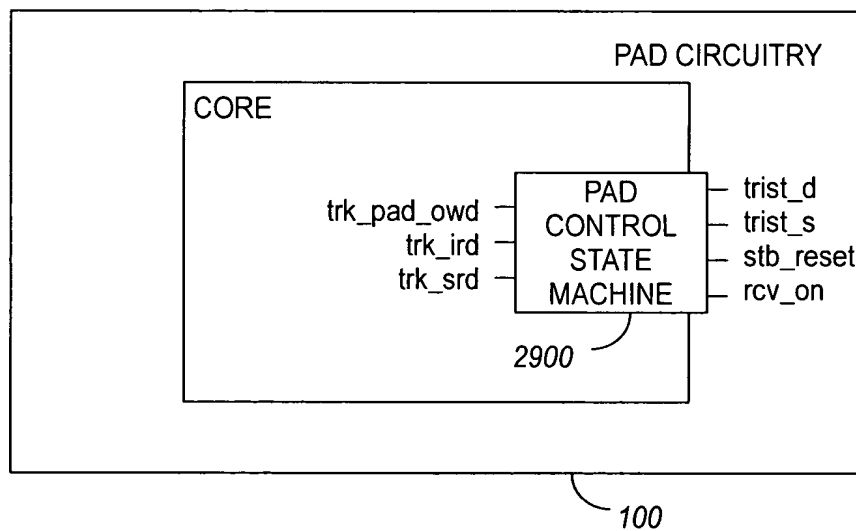


FIG. 29